

**REMARKS**

The Attorney for the Applicants wishes to thank the Examiner for the telephone interview that took place on February 4, 2004 and for the helpful suggestions offered by the Examiner. Based on this interview, the Applicants have amended apparatus claim 19 to remove the process like limitation from the claim. The Applicants have also removed the "at least partially" limitation of the claim as suggested by the Examiner.

Claims 1-18 have been cancelled by this amendment.

The Applicants submit that claim 19 is now allowable. Although patentable in their own right, claims 20 - 34 and 38-42 are also patentable based on their dependency on claim 19.

The Applicants submit that claim 35 is also allowable. Claim 35 is directed to a semiconductor *wafer* with a layer of at least partially cured underfill adhesive formed on the active surface. In contrast, Nishiguchi teaches the filling of the gap between a semiconductor device and a substrate with a bonding agent. Specifically, the "bonding agent" of Nishiguchi is applied *after* the semiconductor device is mounted onto the substrate. Specifically, column 3 lines 46-52 state:

Instead of molting the bump 2, insulative bonding agent which contracts when it cures may be filled into a gap between the semiconductor device 1 and the substrate 3 and the bump 2 may be pushed to the electrode terminal 5 by a curing contraction force of the bonding agent to electrically connect the bump 2 to the electrode terminal 5. (emphasis added)

Nishiguchi therefore explicitly teaches that the bonding agent is applied subsequent the semiconductor device being mounted onto the substrate. After mounting, the agent is then introduced into the gap.

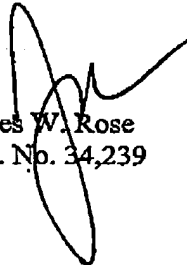
Holzapfel is directed to a polishing machine for removing layers from a wafer. There is absolutely no reason whatsoever to combine these references. In fact, the two references seem to contradict one another, and therefore can not be combined as the Examiner suggests. One is

directed to forming a layer (bonding agent) onto a single semiconductor device. The other is directed to removing layers from the surface of a wafer.

The Applicants submit that claim 35 is therefore allowable. Although patentable in their own right, claims 36-37 are also allowable based on their dependency on claim 35. Finally, although not specifically addressed herein because it is believed not necessary, the Applicants disagree with the Examiner's reasons for rejecting claims 36-37. The Applicants failure to substantively address these rejections should not be construed in anyway as an admission by the Applicants as to the veracity of these rejections. The Applicants reserve the right to address these rejections at anytime in the future.

Applicant believes that all pending claims are allowable and respectfully requests a Notice of Allowance for this application from the Examiner. Should the Examiner believe that a telephone conference would expedite the prosecution of this application, the undersigned can be reached at the telephone number set out below.

Respectfully submitted,  
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